

In the Specification

On page 1, after the title insert:

CROSS REFERENCE TO RELATED APPLICATION

A, This patent application is a Divisional Application of U.S. Patent Application Serial No. 09/388,857, filed September 1, 1999, entitled "Semiconductor Processing Methods Of Forming Transistors, Semiconductor Processing Methods Of Forming Dynamic Random Access Memory Circuitry, And Related Integrated Circuitry", naming Luan C. Tran as inventor, the disclosure of which is incorporated by reference.

Replace the paragraph beginning on page 10, line 21, and extending through p. 11, line 9 with the following:

Ar Also shown in Fig. 7 is a sense amplifier circuit 50 including cross-coupled transistors 52 and 54. In one embodiment, the transistors 52 and 54 are formed to have a low threshold voltage V_{th} . When the signal CSAL goes to logic "1", the common node labeled RNL* equilibrates the potentials on sources of the transistors 52 and 54 in preparation for reading stored data from memory cells in a memory array (not shown). In the example shown in Fig. 7, the circuit 40 acts as a pull-down circuit and equilibrates the node RNL* to ground. Use of multiple transistors 42, 44 and 46 having different threshold voltages facilitates ("softens") sensing at the beginning of the sensing cycle and also facilitates more rapid sensing at the end of the cycle when differential signals have been developed by the transistors 52 and 54.

In the Drawings

Please modify Fig. 3 as shown in the enclosed marked-up-in-red copy thereof. Substitute formal drawings are also enclosed, including a corrected

Fig. 3.

In the Claims

Please cancel claims 1-7 without prejudice.